

Electronic Acknowledgement Receipt

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Application Number:	10807478
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Title of Invention:	Method of checking the layout versus the schematic of multi-fingered MOS transistor layouts using a sub-circuit based extraction
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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Amendment After Final	FIS920040073US1AMENDMENT.pdf	422013	no	12
Warnings:					
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